

Dr. V. Chandra Sekhar

(Vorugunti Chandra Sekhar)

Lead Engineer, Samsung Electro-Mechanics (Bangalore) · Deep Learning Researcher & Educator

Profile

Dr. Chandra Sekhar was a Lead Engineer at Samsung Electro-Mechanics with 16 years of industry experience spanning applied deep learning, MLOps, and Agentic AI in real-time manufacturing environments. He holds a **PhD in Deep Learning from IIIT SriCity** (thesis on efficient online signature verification), an MS from DA-IICT — among India's top technology schools — and a B.Tech in Computer Science from JNTU Hyderabad.

During his doctoral work he spent roughly 30 months as a visiting research scholar across **IIT Madras** (with Prof. V. Srinivasa Chakravarthy's computational neuroscience group), **IIT Tirupati**, and **IIT Indore**, working on deep learning, few-shot learning, and biometric signal modelling.

Teaching & Executive Training

He has trained **3,000+ senior managers and executives** in AI, ML, DL, MLOps, and Agentic AI through programs run by IISc, IIT Madras, IIT Hyderabad, IIIT Hyderabad, NPCI, and Google. As visiting and lead faculty he has taught Machine Learning, Deep Learning, Reinforcement Learning, Distributed ML, Cloud Computing, and Data Engineering at BITS-Pilani, IIT Tirupati, IISER Bhopal, IIIT Lucknow, IIIT SriCity, IIIT-DM Jabalpur, CMI, Krea University, and SSSIHL.

Research

His research centers on deep learning — CNN architectures, GANs, transformers, few-shot and curriculum learning, and model compression — with **125+ publications** at venues including CVPR, ICCV, WACV, ICDAR, and IJCB, over **500 citations**, and an **h-index of 13**.

Recognition

Silver Award, Samsung Annual Awards (2022); recognized among Samsung India's Best AI Trainers; multiple best-paper and best-poster awards at international conferences; and author of the book *Fun with C#*.

Relevance to This Session

For this course, Dr. Chandra Sekhar brings a practitioner's view of how modern ML and DL methods — representation learning, sequence models, and generative architectures — apply to signal-rich domains such as neuroscience, grounded in both large-scale industrial deployment and years of graduate-level teaching.